

University / Academy: Menoufiya University

College / Institute: Faculty of Electronic Engineering

Department: Computer Science and Engineering

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## Course Specification

1- Course basic information :		
Course Code: CSE 061	Course Title: Computer Fundamentals	Academic year: 2011/2012 Level (0) – Semester : 1
University requirement	Teaching hours: Lecture <input type="text" value="2"/> Tutorial <input type="text" value="0"/> Lab <input type="text" value="2"/>	

2- Aim of the course	<ul style="list-style-type: none"><li>• Understand the principles of computer units, logic gates and logic circuits.</li><li>• Understand the methods of logic circuit reduction</li><li>• Understand how to design a logic circuits</li></ul>
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3- Intended Learning Outcomes:	
A- Knowledge and Understanding:	a2. Basics of information and communication technology (ICT). a4. Principles of design including elements design, process and/or a system related to specific computer science and engineering. a13. Engineering principles in the fields of logic design.
B- Intellectual Skills	b3. Think in a creative and innovative way in problem solving and design. b8. Select and appraise appropriate ICT tools to a variety of engineering problems.

<b>C- Professional Skills</b>	c1. Apply knowledge of mathematics, science, information technology, design, business context and engineering practice integrally to solve engineering problems. c4. Practice the neatness and aesthetics in design and approach. c13. Design and operate computer-based systems specifically designed for business applications.
<b>D- General Skills</b>	d1. Collaborate effectively within multidisciplinary team. d4. Demonstrate efficient IT capabilities. d9. Refer to relevant literatures.
<b>4- Course Contents</b>	Number Systems: Binary, Octal, and Hexadecimal number systems – Computer Arithmetic - Logic gates - Basics of digital circuits and the simple combined logic elements - Boolean Algebra and Logic Simplification - Flip-Flops and Sequential logic Circuits – Registers and Counters.
<b>5- Teaching and Learning Methods</b>	<ul style="list-style-type: none"> <li>- Lectures</li> <li>- Labs and/or case studies</li> <li>- Research assignments</li> </ul>
<b>6- Teaching and Learning Methods for disable students</b>	<ul style="list-style-type: none"> <li>- NA</li> </ul>
<b>7- Student Assessment</b>	
<b>a- Assessment Methods</b>	<ul style="list-style-type: none"> <li>- Weekly sheet exercises at class room</li> <li>- Quizzes</li> <li>- Labs and/or case study for more demonstration.</li> <li>- Mid term, and final exams</li> </ul>
<b>b- Assessment Schedule</b>	<ul style="list-style-type: none"> <li>- Exercise sheet/ Lab assignment : Weekly</li> <li>- Quiz-1: Week no 3</li> <li>- Mid-Term exam: Week no 8</li> <li>- Quiz-2: Week no 11</li> <li>- Lab exam: Week no 15</li> <li>- Final – term examination: Week no ---</li> </ul>
<b>c- Weighting of Assessment</b>	<ul style="list-style-type: none"> <li>- Class tutorial and quizzes: 5 %</li> <li>- Mid-term examination: 10 %</li> <li>- Case study and/or practical exam: 15 %</li> <li>- Final – term examination: 70 %</li> <li>- Other types of assessment: ..... %</li> </ul>

	Total 100 %
<b>8- List of text books and references:</b>	
<b>a- Course notes</b>	There are lectures notes prepared in the form of a book authorized by the department
<b>b- Text books</b>	[1] Thomas Floyd, " Digital fundamental" Prentice-Hall, Inc., 1997
<b>c- Recommended books</b>	<b>None</b>
<b>d- Periodicals, Web sites .....etc</b>	IEEE transactions on computers and software.

### Course contents - ILOs Matrix

Content Topics	Week	A- Knowledge & Understanding	B- Intellectual skills	C- Professional and practical skills	D- General and transferable skills
Number Systems	1 - 2	a2, a4	b3, b8	c1	d1, d4
Computer Arithmetic	3	a13	b3, b8	c1, c4	d1, d4
Logic gates	4	a4, a13	b3, b8	c1, c4	d1, d4
Basics of digital circuits and the simple combined logic elements	5-6	a4, a13	b3, b8	c1, c4	d1, d4
Boolean Algebra	7	a4, a13	b3, b8	c1, c4	d1, d4, d9
Logic Simplification	9	a4, a13	b3, b8	c1, c4	d1, d4, d9
Flip-Flops	10-11	a4, a13	b3, b8	c1, c4, c13	d1, d4, d9
Sequential logic Circuits	13	a4, a13	b3, b8	c1, c4, c13	d1, d4, d9
Registers and Counters	13-14	a4, a13	b3, b8	c1, c4, c13	d1, d4, d9

**Course coordinator:**

**Prof. Dr. Nawal El-Fishawy**

**Dr. Ahmed Shouman**

**Date: / 09 / 2012**

**Head of Department:**

**Prof. Dr. Nawal El-Fishawy**